[FIG.1]

ONE FRAME

ONE SLOT

- 5 [FIG.4]
 - 110 DESPREADING SECTION
 - 120 COHERENT DETECTION SECTION
 - 130 RAKE COMBINING SECTION
 - 160 CHANNEL ESTIMATION SECTION
- 10 170 ESTIMATE VALUE CORRECTION SECTION
 - 140 PHASE ROTATION SECTION
 - 150 QPSK DEMAPPING SECTION
 - 152 P/S CONVERSION SECTION

RECEPTION BIT STRING

15

[FIG.5]

PATH 1 PATH 2 PATH 3 PATH 4

TIME

ONE SLOT

20

[FIG.6]

- 110 DESPREADING SECTION
- 120 COHERENT DETECTION SECTION
- 130 RAKE COMBINING SECTION
- 25 150a QPSK DEMAPPING SECTION
 - 152 P/S CONVERSION SECTION

RECEPTION BIT STRING

160 CHANNEL ESTIMATION SECTION

210 ESTIMATE VALUE CORRECTION SECTION

[FIG.7]

- 110 DESPREADING SECTION
- 5 120 COHERENT DETECTION SECTION
 - 130 RAKE COMBINING SECTION
 - 150 QPSK DEMAPPING SECTION
 - 152 P/S CONVERSION SECTION

RECEPTION BIT STRING

- 10 160 CHANNEL ESTIMATION SECTION
 - 210 ESTIMATE VALUE CORRECTION SECTION

[FIG.8]

- 110 DESPREADING SECTION
- 15 120 COHERENT DETECTION SECTION
 - 130 RAKE COMBINING SECTION
 - 160 CHANNEL ESTIMATION SECTION
 - 310 ESTIMATE VALUE CORRECTION SECTION
 - 400 INTERFERENCE CANCELLATION SECTION
- 20 410 CALCULATION VARIABLE GENERATION SECTION
 - 420 INTERFERENCE CANCELLATION CALCULATION SECTION
 - 150 QPSK DEMAPPING SECTION
 - 152 P/S CONVERSION SECTION

RECEPTION BIT STRING

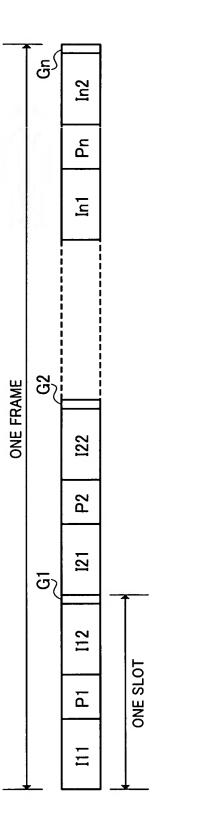


FIG. 1

2/7

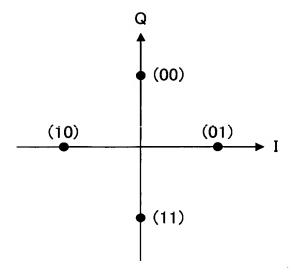


FIG.2

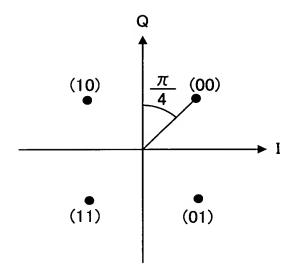
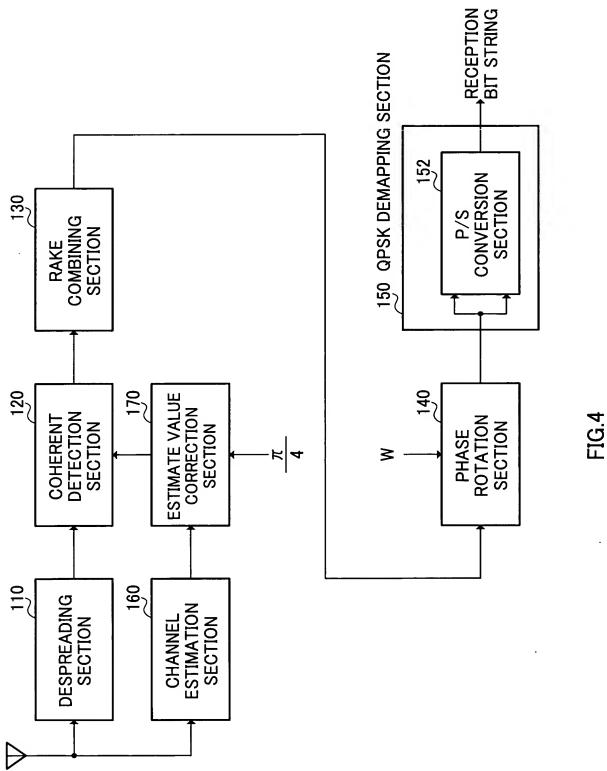


FIG.3



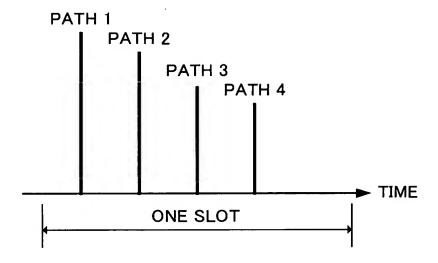


FIG.5

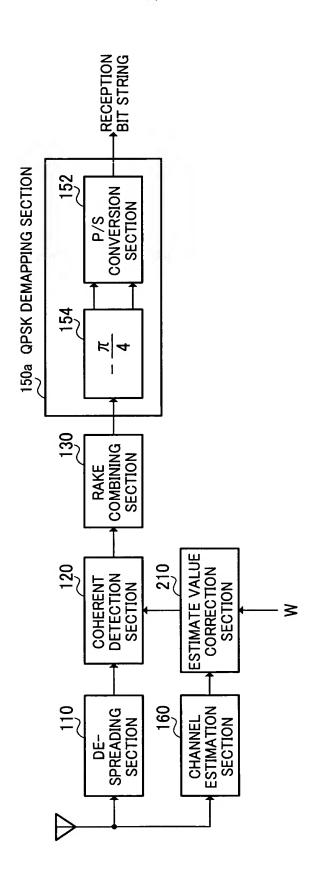


FIG.6

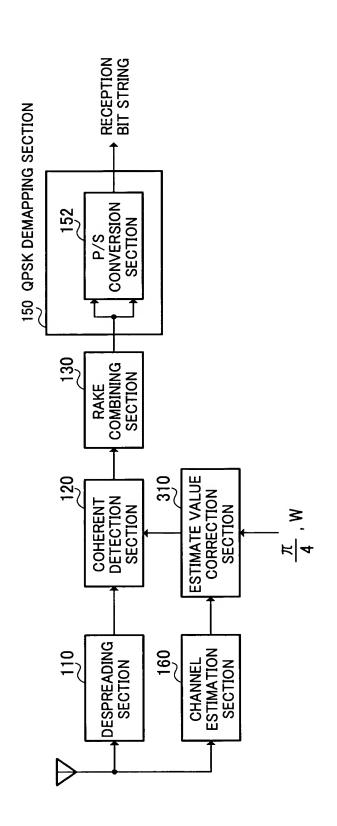


FIG.7

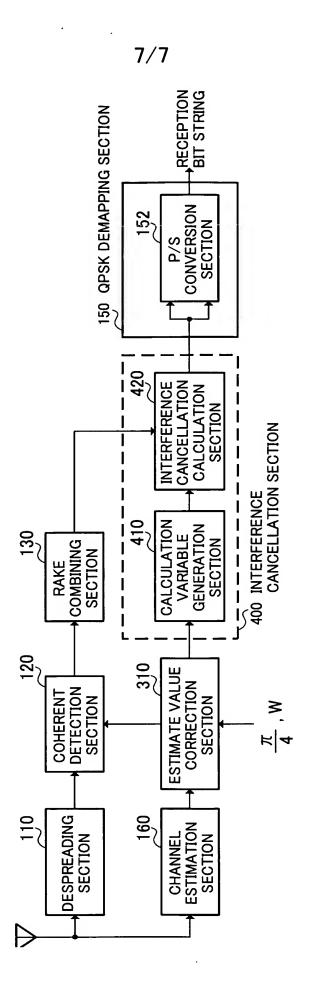


FIG.8